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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/198,849	11/24/1998	YOSHINOBU KANEYAMA	P/1929-47	3012

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EXAMINER

ELVE, MARIA ALEXANDRA

ART UNIT	PAPER NUMBER
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1725

DATE MAILED: 08/26/2003

22

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/198,849

Applicant(s)

KANEYAMA, YOSHINOBU

Examiner

M. Alexandra Elve

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 25 June 2003.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 10-19, 21, 22 and 25-52 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 10-19, 21, 22 and 25-52 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 24 November 1998 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

## Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

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## **DETAILED ACTION**

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

Claims 10-19, 21-22 & 25-52 are rejected under 35 U.S.C. 102(a) as being anticipated by applicant's admitted prior art.

Applicant discloses in the specification that a positional displacement normally occurs during manufacture. Furthermore, admitted prior art, figures 2A and 2B clearly display and thus teach non-alignment.

### ***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the

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invention was made to a person having ordinary skill in the art to which said subject matter pertains.

Patentability shall not be negated by the manner in which the invention was made.

Claims 10-19, 21-22 & 25-52 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP(I) (197112 Abstract) in view of Sliwa, Jr. (US Pat. 4,990,462) and Sherry (US Pat. 4,763,829).

JP(I) discloses a mounting method in which a eutectic alloy solder is used to join semiconductor elements to a substrate whereby the soldering is conducted while immersed in an inactive liquid (vapor), thus eliminating oxidation effects JP(I) does not teach the joining of optical devices, semiconductor or ceramic substrates, printed circuit boards, electrodes, the use of ultrasonic vibration, the breaking of surface oxides or specifically the use of a liquid media.

Sliwa, Jr. discloses the use of liquid surface tension to aid in assembly of integrated circuits, optoelectronic devices, with ceramic and semiconductor substrates (abstract; col. 21-22, lines 66-68 & 1-5 and col. 23, lines 16-26). Additionally, it is noted that the one of key mechanisms of the liquid with respect to assembly are the ability of the liquid to render mating segments coplanar in preparation for joining. This is done by a configuration, which seeks the minimum surface tension (col. 13, lines 33-47). The liquid agent helps control the assembly action forces acting on the segments via surface tension and viscosity selection to minimize mechanical damage (col. 16, lines

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40-68). Flotation liquids may be water, methyl or iso-proryl alcohol, molten indium or other low melting point metals and so forth (col. 18, lines 65-69).

The mother substrate may be a semiconductor, metal or insulation material such as glass or ceramic and may disposed on its surface any desirable combination of receptacle segments and conventional hybrid-style components and interconnects (col. 23, lines 16-21). Additionally, conventional solder bumps are shown on flip-chips (col. 23, lines 42-45).

It would have been obvious to one of ordinary skill in the art at the time of the invention to substitute liquid, as taught by Sliwa, Jr., for the JP(I) vapor because they are functional equivalents in terms of carrier media.

It would have been obvious to one or ordinary skill in the art to use semiconductors, optical devices, semiconductor or ceramic substrates, printed circuit boards and electrodes as taught by Sliwa, Jr. because these are merely varieties of semiconductor elements and substrates as used by JP(I).

Sherry discloses a technique for providing solder bumps to electronic components, such as silicon chips, chip carriers and circuit boards. Solder is applied to the surface along with ultrasonic energy so that the solder wets thoroughly (abstract). That is, a wafer is dipped into solder and at the same time ultrasonic energy is applied to the solder by a commercially available ultrasonic horn (col. 3, lines 4-13). The ultrasonic energy serves to break down the surface tension of the solder, thus

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permitting the solder to penetrate the openings in the mask and wet the exposed pads. Ultrasonic energy also aids in breaking up any oxide on the pad surfaces, thus eliminating the need for a fluxing operation (col. 3, lines 15-21).

It would have been obvious to one of ordinary skill in the art at the time of the invention to use ultrasonic energy for solder bonding as taught by Sherry to the JP(I) soldering operation because it ensures the integrity of the solder joint.

### ***Response to Amendment***

Upon carefully reviewing Applicant's arguments filed June 25, 2003 (paper # 21) the Examiner acknowledges the addition of claims 51-52.

Applicant's arguments with respect to claims 10-19, 21-22 & 25-52 have been considered but are moot in view of the new ground(s) of rejection.

### ***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. JP(II) (3060134A, Abstract) & JP(III) (16902, Abstract).

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Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).


A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to M. Alexandra Elve whose telephone number is (703) 308-0092. The examiner can normally be reached Monday to Friday from 6:30 AM to 3:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Dunn, can be reached on (703) 308-3318. The fax number for the group is (703) 872-9386.

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Any inquiry of general nature to the status of this application or proceeding should be directed to the group receptionist whose telephone number is (703) 308-0661.



M. ALEXANDRA ELVE  
PRIMARY EXAMINER

August 25, 2003.